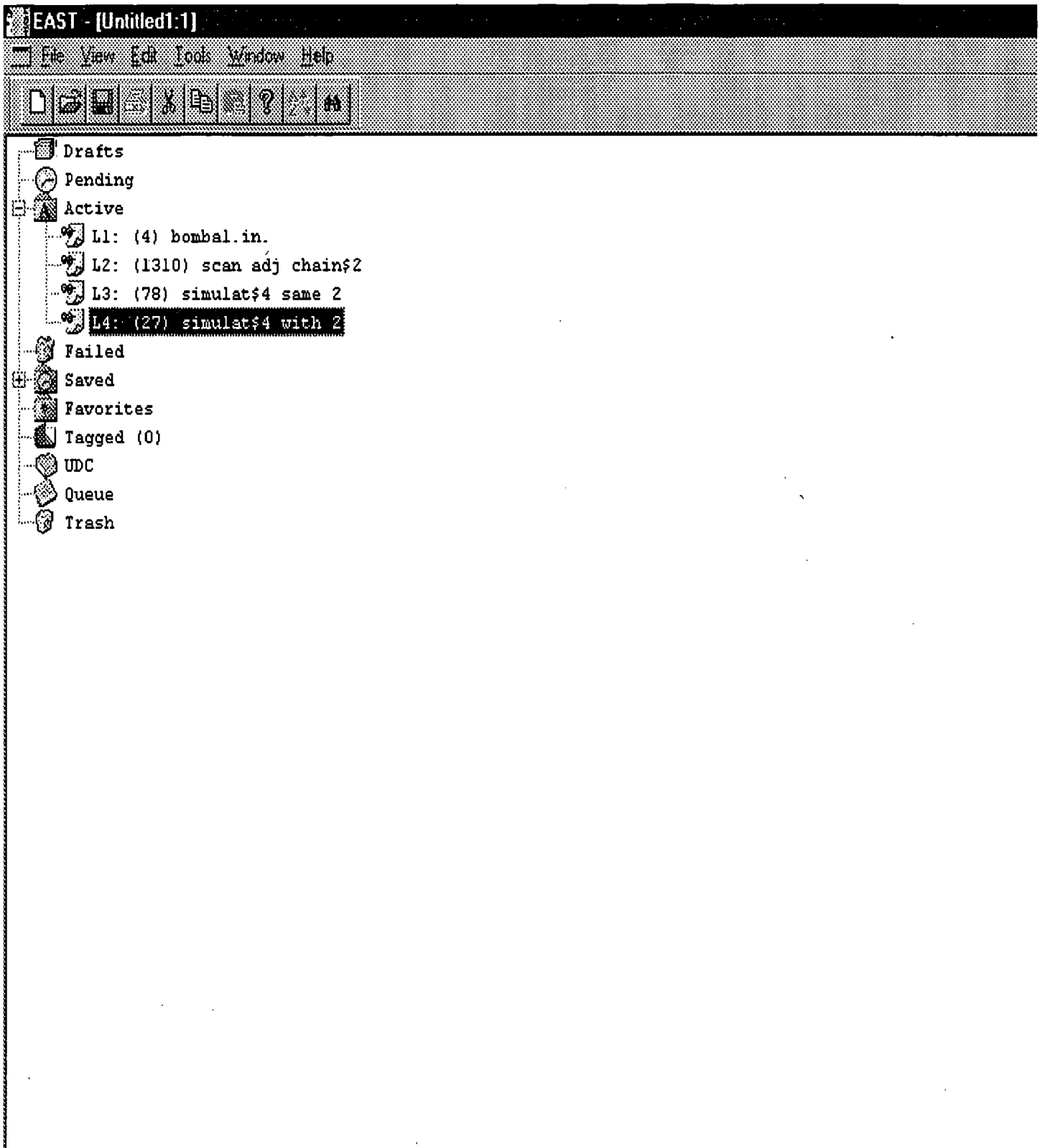

	U	1	Document ID	Issue Date	Pages	Title	
1	☐	☐	US 6826732 B2	20041130	82	Method, system and program product for utilizing a configuration database to configure a hardware digital system	7
2	☐	☐	US 6748352 B1	20040608	11	Method and apparatus for scan design using a formal verification-based process	7
3	☐	☐	US 6708317 B2	20040316	10	Validating integrated circuits	7
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